

U.S. Department of Commerce, Patent and Trademark Office					Attorney Docket No.		Serial No.	
					SUN040316		10/779,327	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Dennis Wendell			
					Filing Date		Group	
					February 13, 2004		<del>Unknown</del> 2827	

**U.S. Patent Documents**

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DL	AA	6,667,921	12/23/2003	Park	365	203	
DL	AB	6,643,804	11/04/2003	Aipperspach et al.	714	718	
DL	AC	5,963,494	10/05/1999	Khang	365	203	
DL	AD	5,883,845	03/16/1999	Khang	365	203	
DL	AE	5,875,139	02/23/1999	Semi	365	203	
DL	AF	5,848,015	12/0/1998	Seno	365	203	
DL	AG	5,563,831	10/08/1996	Ting	365	189.09	
DL	AH	5,007,023	04/09/1991	Kim et al.	365	203	
	AI						
	AJ						
	AK						

**Foreign Patent Documents**

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						<input type="checkbox"/>	<input type="checkbox"/>
	AM						<input type="checkbox"/>	<input type="checkbox"/>
	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

DL	AR	Bhutani, S., et al., "Processor Having a Coalescing Cache Disable Mode," U.S. Patent Application Serial No. 10/766,745, filed January 27, 2004
DL	AS	Heald, R., et al., "A Third-Generation SPARC V9 64-b Microprocessor," IEEE JSSC, Nov. 2000, pp. 1526-1538
DL	AT	Konstadinidis, G., et al., "Implementation of a Third-Generation 1.1GHz 64b Microprocessor," 2002 IEEE International Solid-State Circuits Conference, Session 20/Microprocessors/20.3
DL	AU	Konstadinidis, G., et al., "Implementation of a Third-Generation 1.1GHz 64b Microprocessor," 2002 IEEE Journal of Solid-State Circuits, Vol. 37, No. 11, Nov. 2002, pp. 1461-1469

Examiner *[Signature]* Date Considered *6/05*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

5/18/04

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	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
DL	AR	McCarroll, B., et al., "A High-Speed CMOS Comparator for Use in an ADC," IEEE JSSC, Feb. 1988, pp. 159-165						
DL	AS	Wendell, D., et al., "A 4 MB On-Chip L2 Cache for a 90nm 1.6GHz 64 bit Microprocessor," IEEE JSSC, Feb. 2004						
	AT							
	AU							
Examiner		<i>D.A. Jones</i>			Date Considered		6/05	
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								